

DERWENT-ACC-NO: 1988-134476

DERWENT-WEEK: 199845

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TITLE: Time-counter using register block -
has operation
controller receiving control
information for generating
control signals for selected operation
and has count
registers

INVENTOR: AKASHI, M; AKITA, I

PATENT-ASSIGNEE: NEC CORP[NIDE]

PRIORITY-DATA: 1986JP-0270144 (November 12, 1986)

PATENT-FAMILY:

PUB-NO	PAGES	MAIN-IPC	PUB-DATE	LANGUAGE
EP 267612 A	014	N/A	May 18, 1988	E
DE 3752211 G	000	G06F 009/46	October 1, 1998	N/A
JP 63123218 A	000	N/A	May 27, 1988	N/A
US 4939755 A	013	N/A	July 3, 1990	N/A
EP 267612 B1	000	G06F 009/46	August 26, 1998	E

DESIGNATED-STATES: DE FR GB IT DE FR GB IT

CITED-DOCUMENTS: 1.Jnl.Ref; A3...199017 ; EP 175603 ;
No-SR.Pub ; US 3633181
; US 4024510 ; US 4220990

APPLICATION-DATA:

PUB-NO	APPL-DATE	APPL-DESCRIPTOR	APPL-NO
EP 267612A	November 12, 1987	N/A	1987EP-0116725

DE 3752211G	N/A	1987DE-3752211
November 12, 1987		
DE 3752211G	N/A	1987EP-0116725
November 12, 1987		
DE 3752211G	Based on	EP 267612
N/A		
JP 63123218A	N/A	1986JP-0270144
November 12, 1986		
US 4939755A	N/A	1987US-0119515
November 12, 1987		
EP 267612B1	N/A	1987EP-0116725
November 12, 1987		

INT-CL (IPC): G06F001/00, G06F007/02 , G06F009/46 ,
 G06F015/06 ,
 H03K017/28 , H03K021/38 , H03K023/66

ABSTRACTED-PUB-NO: EP 267612A

BASIC-ABSTRACT:

The timer/counter has an operation controller coupled to receive a control information for generating control signals for a selected operation, and a register block coupled to a bus and has a number of count registers. A corresponding number of timer registers store various set values and a buffer circuit is controlled by the operation controller to sequentially read data from one of the count registers to the bus and then write the data on the bus to the same count register.

An incrementer is coupled to the bus and controlled by the operation controller to increment the data on the bus and to output an incremented data to the bus. A coincidence flag coupled to the register block is set when the incremented data in a count register of the time block has coincided with a value stored in a corresponding timer register. A clear controller coupled to the coincidence flag clears the count register having the count value in coincidence with the

value of the corresponding timer register.

ADVANTAGE - Can create number of timer functions.

ABSTRACTED-PUB-NO: EP 267612B

EQUIVALENT-ABSTRACTS:

The timer/counter has an operation controller coupled to receive a control information for generating control signals for a selected operation, and a register block coupled to a bus and has a number of count registers. A corresponding number of timer registers store various set values and a buffer circuit is controlled by the operation controller to sequentially read data from one of the count registers to the bus and then write the data on the bus to the same count register.

An incrementer is coupled to the bus and controlled by the operation controller to increment the data on the bus and to output an incremented data to the bus. A coincidence flag coupled to the register block is set when the incremented data in a count register of the time block has coincided with a value stored in a corresponding timer register. A clear controller coupled to the coincidence flag clears the count register having the count value in coincidence with the value of the corresponding timer register.

ADVANTAGE - Can create number of timer functions.

US 4939755A

The timer/counter comprises an operation controller coupled to receive a control information for generating control signals for a selected operation, and a register block coupled to a bus and including several count registers.

A corresponding number of timer registers store various set

values and a bufferr circuit controlled by the operation controller sequentially reads data from one of the count registers to the bus and then writes the data on the bus to the same count register. An incrementer is coupled to the bus and controlled by the operation controller to output an incremented data to the bus. A coincidence flag coupled to the register block is set when the incremented data in a count register of the timer block has coincided with a value stored in a corresponding timer register. A clear controller coupled to the coincidence flag clears the count register having the count value in coincidence with the value of the corresponding timer register.

(13pp)

CHOSEN-DRAWING: Dwg.1/6

DERWENT-CLASS: T01

EPI-CODES: T01-F02;